

MEASUREMENT AND ANALYSIS OF I-V-T CHARACTERISTICS OF A AUGENI/P-SI SCHOTTKY BARRIER DIODE

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ABSTRACT

In this work, we report measured forward current voltage characteristics of AuGeNi/p-Si schottky barrier diode in the temperature range of 295-400 °K. Forward current voltage characteristics were investigated. This investigation is based on the analysis of the dependency of extracted parameters such as ideality factor (n), barrier height (ϕ_{B0}) and saturation current (I_0) on temperature. The Richardson coefficient was examined by means of the saturation versus temperature. While n decreases, ϕ_{B0} increases with increasing temperature. Obtained Richardson constant value of the $A^*=11.5 \times 10^{-7} \text{ Acm}^{-2}\text{K}^{-2}$ is very low compared to the standard value. The modified Richardson plot has given mean barrier height ϕ_{B0} and Richardson constant (A^*) as 1.15 eV and $30.53 \text{ Acm}^{-2}\text{K}^{-2}$, respectively. The temperature dependence of the I-V characteristics of the AuGeNi/p-Si Schottky diode have been successfully explained on the basis of thermionic emission (TE) mechanism with Gaussian distribution of the Schottky barrier heights (SBHs).

KEYWORDS: Schottky contacts, current-voltage-temperature characteristics, Gaussian distribution, Barrier inhomogeneity.

1 INTRODUCTION

The contact between silicon and metal plays an important role in integrated circuit technology. The physics involved in this contact has been interpreted using the Schottky barrier concept. When the doping level in the silicon is low, the Schottky barrier acts as a diode that has a lower turn-on voltage than a p-n junction diode. With this advantage, the Schottky diode has been frequently used in high-speed devices such as those in GaAs [1,2]. The low noise level generated by Schottky diodes makes them especially suitable for uses in microwave receivers, detectors, photo transistors and space solar cells [3,4]. The important aspect of metal-semiconductor junctions is the process, which determines the flow of charge carriers over the barrier from the semiconductor to the metal and vice versa. Detailed knowledge of the conduction process involved is essential to extract barrier parameters, namely barrier height, ideality factor and series resistance.

Analysis of the current-voltage (I-V) characteristics of the Schottky barrier measured only at room temperature does not give detailed information about the conduction process and the nature of barrier formed at the metal semiconductor interface. In fact, it neglects many possible effects that cause non-ideality in the diode I-V characteristics and, in general, reduce the barrier height. The temperature

dependence of the I-V characteristics gives a better picture of various conduction mechanisms and on the validity of various processes involved. Moreover, Schottky diodes with low barrier heights have found applications in devices operating at cryogenic temperatures as infrared detectors and sensors in thermal imaging [5, 6].

The current-voltage (I-V) characteristics of the metal-semiconductor contacts usually deviate from the ideal thermionic emission (TE) current model [2, 6], and the ideality factor should be equal to unity ($n=1$). However, due to various factors such as device temperature, dopant concentration, device area, density of interface states, structural properties of interface etc., the current-voltage characteristics of Schottky contact exhibit deviations from TE-mechanism with temperature dependent ideality factor [1-3, 9] [7].

The analysis of the I-V characteristics of the Schottky contacts based on TE theory usually reveals an abnormal decrease in the BH and increase in the ideality factor with decrease in temperature [1,5,6].

2 EXPERIMENTAL METHOD

The structure AuGeNi/Si is obtained on sample of p-type Si

wafer of (100)orientation and 1 – 20 cm resistivity which have been subjected to chemical etching. For metal/p type Si Schottky contact, the AuGeNi is deposited by thermal evaporation with diameters of about 1.5 mm on the front surface of the wafer. The ohmic contact is obtained by evaporation of the eutectic AuGeNi. The current-voltage characteristics (I-V) are recorded at different temperatures (295K-400K) on the sample using a Keithley 2400 source-meter. The temperature was adjusted using Janis vpf-475 cryostat (SMU) Keithley 236.

3 RESULTS AND DISCUSSION

Fig.1 shows the experimental typical forward and reverse current-voltage characteristics of AuGeNi/p-Si Schottky diodes in the temperature range of 295–400 K. For Schottky diode, the thermionic emission theory predicts that the current-voltage characteristic is given by [8, 9].

$$I = I_0 \left[\exp\left(\frac{qV}{\eta KT}\right) - 1 \right] \quad (1)$$

And

$$I_0 = AA^*T^2 \exp\left(\frac{-q\phi_{B0}}{KT}\right) \quad (2)$$

Where I_0 is the saturation current, ϕ_{B0} is the zero-bias barrier height, A^* is the effective Richardson constant, A is the diode area, T is the temperature in Kelvin, K is the Boltzmann's constant, q is the electronic charge and η is the ideality factor which describes the deviation of the practical diodes from the pure thermionic emission theory.

The values of saturation current I_0 and ideality factor η are obtained from the linear portion of $\ln(I)$ vs V plot at various temperature. The values of zero-bias barrier height at each temperature are determined from saturation current (I_0) using eq. (2) with $A^*=32A_{cm-2}K^{-2}$ for p-type silicon[10, 11] and A is the diode area.

Once I_0 is determined, the zero-bias barrier height (ϕ_{B0}) is obtained by rewriting Eq. (2) as:

$$\phi_{B0} = \frac{KT}{q} \ln\left(\frac{AA^*T^2}{I_0}\right) \quad (3)$$

From Eq. (1), ideality factor η can be written as:

$$\eta = \frac{q}{KT} \left(\frac{d(V)}{d \ln(I)} \right) \quad (4)$$

Using Eqs. (3) and (4) the values of zero-bias barrier height ϕ_{B0} and the ideality factor η of the diode at different temperatures are calculated and are plotted as a function of temperature (Fig. 2). The ideality factor has been found to increase, while the zero-bias barrier height ϕ_{B0} decrease with decreasing temperature, as seen from these values are given in Table 1. As can be seen in Fig.2, the experimental value of ϕ_{B0} decreased with decrease in temperature.

For the evaluation of barrier height, one may also make use of the Richardson plot of the saturation current, equation (3) can be rewritten as:

$$\ln\left(\frac{I_0}{T^2}\right) = \ln(AA^*) - \frac{q\phi_{B0}}{KT} \quad (5)$$

As shown in Fig. 3 the temperature dependence of $\ln(I_0/T^2)$ versus $1/T$ is found to be linear in temperature range of 295-400K.

The value of A^* obtained from the intercept of the linear portion of the ordinate is $A^*=11.5 \times 10^{-7} A_{cm-2}K^{-2}$, which is much lower than the known value of $A^*=32A_{cm-2}K^{-2}$, barrier height value of 0,135 eV is obtained from the slope of the straight line.

The deviation of the Richardson plots may be due to the spatially inhomogeneous barrier heights and potential fluctuations at the interface that consists of low and high barrier areas.

The obtained value of A^* is low since it is calculated from the temperature-dependent I-V characteristics which may be affected by lateral inhomogeneity of the barrier. A^* is also different from the theoretical. [6, 12, 13, 14, 15].

These results confirm that the predominant current transport is not only the TE in our sample; rather, there are also Gaussian distributions barrier heights in the constant area.

The Gaussian distribution of the BHs is given by the following expression [8, 12, 13, 15].

$$\phi_{ap} = \phi_{B0}(T=0) - \frac{q\sigma_b^2}{2KT} \quad (6)$$

Table 1 : The experimental parameters obtained for AuGeNi/p-Si in the temperature range 295–400K

T (K)	I_0 (A)	η	ϕ_{B0} (eV)
295	2,78E-05	2,00965	0,55611
320	4,23E-05	1,78508	0,59615
340	5,77E-05	1,56802	0,62786
360	9,23E-05	1,49605	0,65377
380	9,80E-05	1,27016	0,69167
400	1,47E-04	1,12833	0,71745

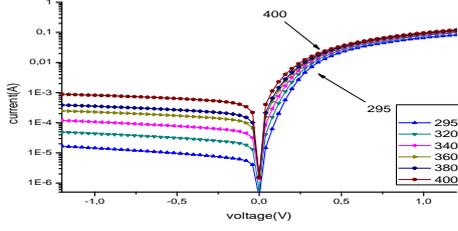


Figure 1: Experimentally Current-Voltage characteristics of the AuGeNi/p-Si Schottky barrier diode at various temperatures.

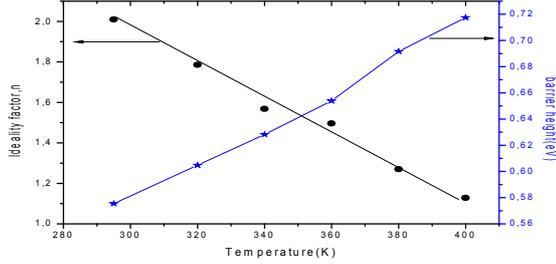


Figure 2: Temperature dependence diode in the temperature range 295–400K of the ideality factor and zero-bias barrier height for AuGeNi/p-Si Schottky.

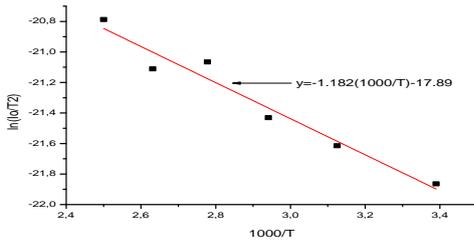


Figure 3: Plot of $\ln(I_0/T^2)$ vs $1000/T$ of AuGeNi/p-Si Schottky Diode

Where ϕ_{B0} and σ_0 are the zero-bias mean barrier height and zero-bias standard deviation.

The temperature dependence of σ_0 is usually small and can be neglected. The observed variation of ideality factor with temperature is given by [12].

$$\left(\frac{1}{n_{ap}} - 1\right) = -\rho_2 + \frac{q\rho_3}{2KT} \quad (7)$$

Where n_{ap} is apparent ideality factor and the coefficients ρ_2 and ρ_3 quantify the voltage deformation of the BH distribution.

However, it is assumed that the standard deviation σ and the mean value of the SBH ϕ_{B0} are linearly dependent on Gaussian parameters, given by $\phi_{ap} = \phi_{B0} + \rho_2 V$ and $\sigma_s = \sigma_0 + \rho_3 V$, where ρ_2 and ρ_3 are the voltage coefficients that may depend on temperature (T) and quantify the voltage deformation of the BH distribution [3, 12, 16]. It is evident that the decrease of zero-bias BH caused by the existence of the Gaussian distribution and the

extent of influence determined by the standard deviation itself.

The plot of ϕ_{ap} versus $1/T$ (Fig. 4) should be straight line with the intercept at the ordinate determining the zero-bias mean BH (ϕ_{B0}) and a slope giving the standard deviation σ_0 . According to Fig.4, the value of $\phi_{B0} = 1.16$ eV and $\sigma_0 = 176$ mV are obtained from the ϕ_{ap} versus $1/T$ plot and in the Fig 5, the plot of 'ap' versus $1/T$ must be a straight line that gives voltage coefficients 2 and 3 from the intercept and slope of the curve respectively. The values of $\rho_2 = -0.89$ V and $\rho_3 = -0.073$ V are obtained from the versus $1/T$ plot.

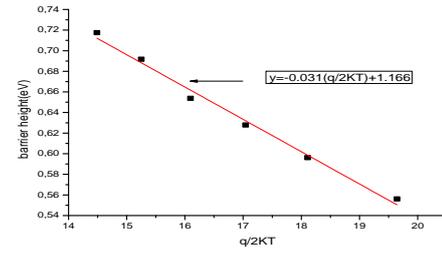


Figure 4: Zero-bias barrier height ϕ_{ap} versus $1/(2kT)$ curve of the AuGeNi/p-Si Schottky diode.

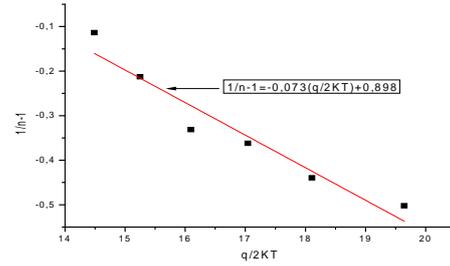


Figure 5: Temperature dependence of the ideality factor 'ap' for AuGeNi/p-Si Schottky diode.

The predictable Richardson plot is now modified by combining Eqs. (2) and (6) as:

$$\ln\left(\frac{I_0}{T^2}\right) - \left(\frac{q^2 \sigma_0^2}{2K^2 T^2}\right) = \ln(AA^*) - \frac{q\phi_{B0}}{KT} \quad (8)$$

A modified $\ln\left(\frac{I_0}{T^2}\right) - \left(\frac{q^2 \sigma_0^2}{2K^2 T^2}\right)$ versus $1/T$ plot according to Eq. (8) should give a straight line with the slope directly yielding the mean ϕ_{B0} and the intercept ($= \ln(AA^*)$) at the ordinate determining A^* for a given diode area A. As shown in Fig. 6, the modified

Richardson plot of $\ln\left(\frac{I_0}{T^2}\right) - \left(\frac{q^2 \sigma_0^2}{2K^2 T^2}\right)$ versus $1/T$ gives $A^* = 30,53 \text{ Acm}^{-2}\text{K}^{-2}$ and $\phi_{B0} = 1.15$ eV respectively, without using the temperature coefficient of the flat-band BH. The value $\phi_{B0} = 1.15$ eV is obtained from $\ln\left(\frac{I_0}{T^2}\right) - \left(\frac{q^2 \sigma_0^2}{2K^2 T^2}\right)$ versus $1/T$ plot. This

value is close to the value $\phi_{B0} = 1.16$ eV obtained from the ϕ_{BP} versus $1/T$ (Fig. 4).

Also, the value of the modified Richardson constant is $A^* = 30.53 \text{ Am}^{-2} \text{ K}^{-2}$, which is close to the theoretical value $A^* = 32 \text{ Am}^{-2} \text{ K}^{-2}$.

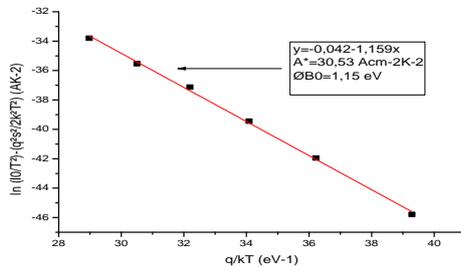


Figure 6: Modified Richardson $\ln\left(\frac{I_0}{T^2}\right) - \left(\frac{q^2 \sigma_0^2}{2K^2 T^2}\right)$ versus $(1/T)$ plot for AuGeNi/p-Si Schottky diode.

4 CONCLUSIONS

The current–voltage (I–V) characteristics of AuGeNi/p-Si Schottky barrier diodes have been made in the temperature range of 295–400K. It is found that the I–V barrier heights increases in the range with the ideality factor decreasing with increasing temperature.

The origin and nature of the decrease in the ideality factor and increase in the barrier height with increase in temperature in AuGeNi/p-Si Schottky diodes have been successfully explained on the basis of the thermionic emission with Gaussian distribution of the barrier heights. This behavior is attributed to spatial variations of the BHs.

The Richardson plot $\ln(I_0/T^2)$ versus $(1/T)$ yielded a straight line with an effective BH value of 0.101 eV. The modified $\ln(I_0/T^2) - (q^2 \sigma_0^2 / 2K^2 T^2)$ versus $(1/T)$ plots yielded zero-bias mean BH value of 1.15 eV and Richardson constant as $30.53 \text{ Am}^{-2} \text{ K}^{-2}$ for AuGeNi/p-Si is obtained considering Gaussian distribution of the barrier height, which is close to theoretical value of $32 \text{ Am}^{-2} \text{ K}^{-2}$ for P-Si.

REFERENCES

[1] SankarNaik*, V. Rajagopal Reddy .Temperature dependency and current transport mechanism of Pd/V/n-type Inp schootky rectifiers. Adv. Mat. Lett. 2012, 3(3), 188-196.

[2] Moongyu Jang and Junghwan Lee. Analysis of Schottky Barrier Height in Small Contacts Using a Thermionic-Field Emission Model. ETRI Journal, Volume 24, Number 6, December 2002.

[3] Subhash Chand and JitendraKumart. Current-voltage characteristics and barrier parameters of Pd2Si/p-Si (111) Schottky diodes in a wide temperature range. Semicond. Sci. Techno.10 (1995) 1680-1688. Printed

in the UK.

- [4] SHENG S.LI, Semiconductor Physical Electronics, 2nd ed. Gainesville, FL 32611–6130USA.
- [5] S.M. Sze, Physics of Semiconductor Devices, 2nd ed., New York, Wiley, 1981.
- [6] S. Shankar Naik, V. Rajagopal Reddy. Analysis of current-voltage-temperature (I-V-T) and capacitance-voltage-temperature (C-V-T) characteristics of Ni/Au Schottky contacts on n-type InP. Super lattices and Microstructures 48 (2010) 330_342.
- [7] Rajinder Sharma. Temperature Dependence of I-V Characteristics of Au/n-Si Schottky Barrier Diode. Journal of Electron Devices, Vol. 8, 2010, pp. 286-292 .
- [8] H. Altuntas, A. Bengi, U. Aydemir, T. Asar, S.S.Cetin, I. Kars, S. Altindal, S. Ozcelik. Electrical characterization of current conduction in Au/TiO2/n-Si at wide temperature range. Materials Science in Semiconductor Processing 12 (2009)224–232.
- [9] E.H. Rhoderick and R.H. Williams. Metal Semiconductor Contacts, Clarendon Press, Oxford (1988).
- [10] Moloi S J and McPherson. Current-voltage behaviour of Schottky diodes fabricated on p-type silicon for radiation hard detectors. Physica B 404 (2009) 2251-2258.
- [11] G ven C, ankayaa and NazimUc,arb. Schottky Barrier Height Dependence on the Metal Work Function for p-type Si SchottkyDiodes. Z. Naturforsch. 59a, 795 – 798 (2004).
- [12] F.E.Cimilli, M.Sag`lam, H.Efeog`lu, A.T r t. Temperature-dependent current–voltage characteristics of the Au/n-InP diodes with inhomogeneous Schottky barrier height. PhysicaB404 (2009)1558–1562.
- [13] V. Janardhanam, A. Ashok Kumar, V. Rajagopal Reddy, P. Narasimha Reddy. Study of current–voltage–temperature (I–V–T) and capacitance–voltage–temperature (C–V–T) characteristics of molybdenum Schottky contacts on n-InP (1 0 0). Journal of Alloys and Compounds 485 (2009) 467–472.
- [14] H. Korkut, N.Yıldırım, A.Turut. Thermal annealing effects on I–V–T characteristics of sputtered Cr/n-GaAs diodes. Physica B404(2009)4039–4044.
- [15] M. Bhaskar Reddy, A. Ashok Kumar, V. Janardhanam, V. Rajagopal Reddy, P. Narasimha Reddy. Current–voltage–temperature (I–V–T) characteristics of Pd/Au Schottky contacts on n-InP (111). Current Applied Physics 9 (2009) 972–977.
- [16] Ö. Demircioglu, S. Karatas, N. Yıldırım, Ö.F. Bakkaloglu, A. Türüt. Temperature dependent current–voltage and capacitance–voltage characteristics of chromium Schottky contacts formed by electro deposition technique on n-type Si. Journal of Alloys and Compounds 509 (2011) 6433–6439.